

Molding Type Module IGBT, 2-in-1 Package, 600 V and 200 A


Double INT-A-PAK

FEATURES

- High short circuit capability
- 10 μ s short circuit capability
- $V_{CE(on)}$ with positive temperature coefficient
- Maximum junction temperature 150 °C
- Latch-up free
- Low inductance case
- Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT

PRODUCT SUMMARY	
V_{CES}	600 V
I_C at $T_C = 80$ °C	200 A
$V_{CE(on)}$ (typical) at $I_C = 200$ A, 25 °C	1.9 V
Speed	DC to 1 kHz
Package	Double INT-A-PAK
Circuit	Half bridge

TYPICAL APPLICATIONS

- UPS
- Switching mode power supplies
- Electronic welders

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general UPS and SMPS.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C unless otherwise noted)				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V_{CES}		600	V
Gate to emitter voltage	V_{GES}		± 20	
Collector current	I_C	$T_C = 25$ °C	260	A
		$T_C = 80$ °C	200	
Pulsed collector current	$I_{CM}^{(1)}$	$t_p = 1$ ms	400	
Diode continuous forward current	I_F	$T_C = 80$ °C	200	
Diode maximum forward current	I_{FM}	$t_p = 1$ ms	400	
Maximum power dissipation	P_D	$T_J = 150$ °C	1042	
Short circuit withstand time	t_{SC}	$T_C = 125$ °C	10	μ s
RMS isolation voltage	V_{ISOL}	$f = 50$ Hz, $t = 1$ min	2500	V
I^2t -value, diode	I^2t	$V_R = 0$ V, $t = 10$ ms, $T_J = 125$ °C	4900	A ² s

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature.



IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	600	-	-	V
Collector to emitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 200 A, T _J = 25 °C	-	1.9	-	
		V _{GE} = 15 V, I _C = 200 A, T _J = 125 °C	-	2.3	-	
Gate to emitter threshold voltage	V _{GE(th)}	V _{CE} = V _{GE} , I _C = 0.25 mA, T _J = 25 °C	3.5	4.5	5.5	
Collector cut-off current	I _{CES}	V _{CE} = V _{CES} , V _{GE} = 0 V, T _J = 25 °C	-	-	5.0	μA
Gate to emitter leakage current	I _{GES}	V _{GE} = V _{GES} , V _{CE} = 0 V, T _J = 25 °C	-	-	400	nA

SWITCHING CHARACTERISTICS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}	V _{CC} = 300 V, I _C = 200 A, R _g = 4.7 Ω, V _{GE} = ± 15 V, T _J = 25 °C	-	106	-	ns
Rise time	t _r		-	45	-	
Turn-off delay time	t _{d(off)}		-	460	-	
Fall time	t _f		-	51	-	
Turn-on switching loss	E _{on}	V _{CC} = 300 V, I _C = 200 A, R _g = 4.7 Ω, V _{GE} = ± 15 V, T _J = 25 °C	-	4.2	-	mJ
Turn-off switching loss	E _{off}		-	9.0	-	
Turn-on delay time	t _{d(on)}	V _{CC} = 300 V, I _C = 200 A, R _g = 4.7 Ω, V _{GE} = ± 15 V, T _J = 125 °C	-	120	-	ns
Rise time	t _r		-	68	-	
Turn-off delay time	t _{d(off)}		-	510	-	
Fall time	t _f		-	70	-	
Turn-on switching loss	E _{on}	V _{CC} = 300 V, I _C = 200 A, R _g = 4.7 Ω, V _{GE} = ± 15 V, T _J = 125 °C	-	5.1	-	mJ
Turn-off switching loss	E _{off}		-	11.3	-	
Input capacitance	C _{ies}	V _{GE} = 0 V, V _{CE} = 25 V, f = 1.0 MHz	-	13.1	-	nF
Output capacitance	C _{oes}		-	0.71	-	
Reverse transfer capacitance	C _{res}		-	0.38	-	
SC data	I _{SC}	t _{sc} ≤ 10 μs, V _{GE} = 15 V, T _J = 125 °C, V _{CC} = 300 V, V _{CEM} ≤ 600 V	-	650	-	A
Stray inductance	L _{CE}		-	-	20	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.35	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Diode forward voltage	V _F	I _F = 200 A	T _J = 25 °C	-	1.4	1.6	V
			T _J = 125 °C	-	1.6	1.8	
Diode reverse recovery charge	Q _{rr}	I _F = 200 A, V _R = 300 V, di/dt = -6000 A/μs, V _{GE} = -15 V	T _J = 25 °C	-	9	-	μC
			T _J = 125 °C	-	16	-	
Diode peak reverse recovery current	I _{rr}	I _F = 200 A, V _R = 300 V, di/dt = -6000 A/μs, V _{GE} = -15 V	T _J = 25 °C	-	140	-	A
			T _J = 125 °C	-	165	-	
Diode reverse recovery energy	E _{rec}	I _F = 200 A, V _R = 300 V, di/dt = -6000 A/μs, V _{GE} = -15 V	T _J = 25 °C	-	2.4	-	mJ
			T _J = 125 °C	-	4.2	-	



THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range	T_J		-40	-	150	°C
Storage temperature range	T_{STG}		-40	-	125	
Junction to case	IGBT	R_{thJC}	-	-	0.12	K/W
	Diode		-	-	0.27	
Case to sink	R_{thCS}	Conductive grease applied	-	0.03	-	
Mounting torque		Power terminal screw: M6	2.5 to 5.0			Nm
		Mounting screw: M6	3.0 to 6.0			
Weight			325			g

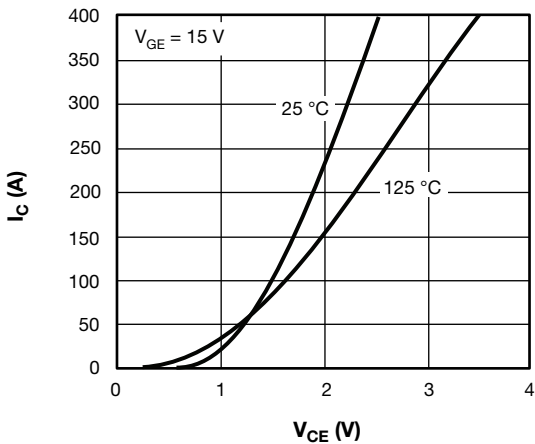


Fig. 1 - Typical Output Characteristics

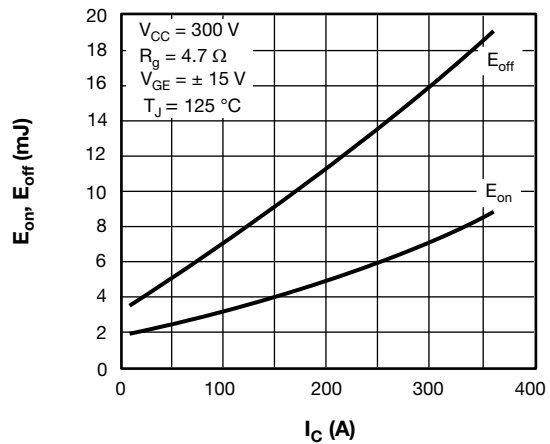


Fig. 3 - Switching Loss vs. Collector Current

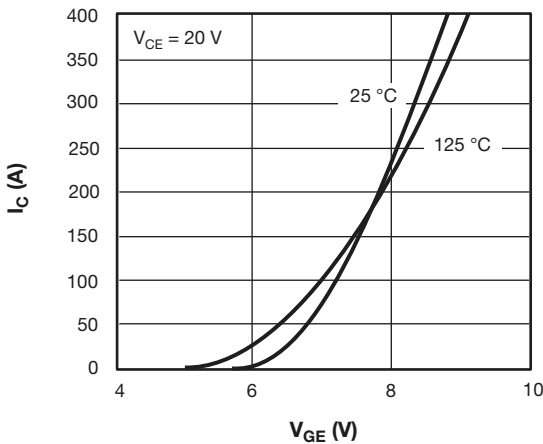


Fig. 2 - Typical Transfer Characteristics

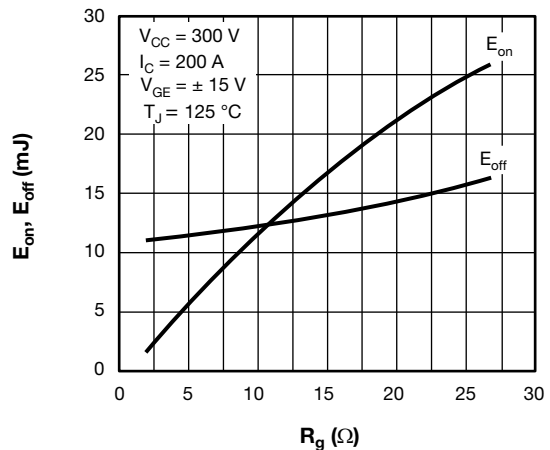


Fig. 4 - Switching Loss vs. Gate Resistor

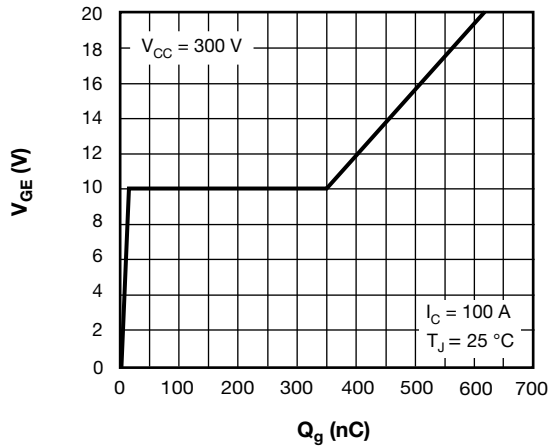


Fig. 5 - Gate Charge Characteristics

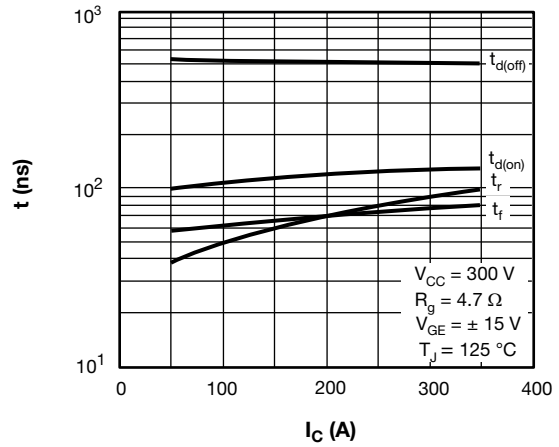


Fig. 7 - Switching Times vs. I_C

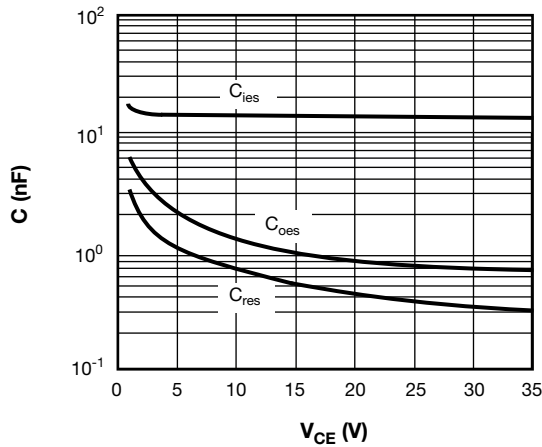


Fig. 6 - Typical Capacitance vs. Collector to Emitter Voltage

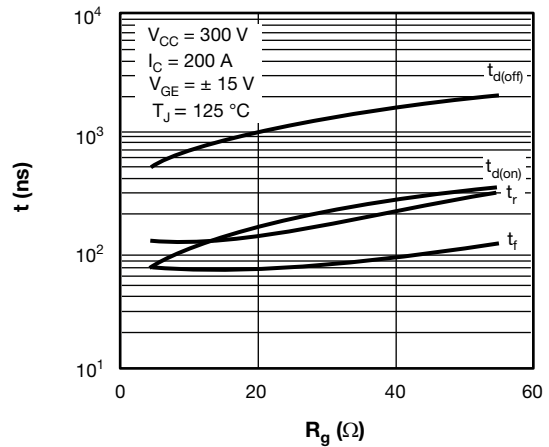


Fig. 8 - Typical Switching Times vs. Gate Resistance R_g

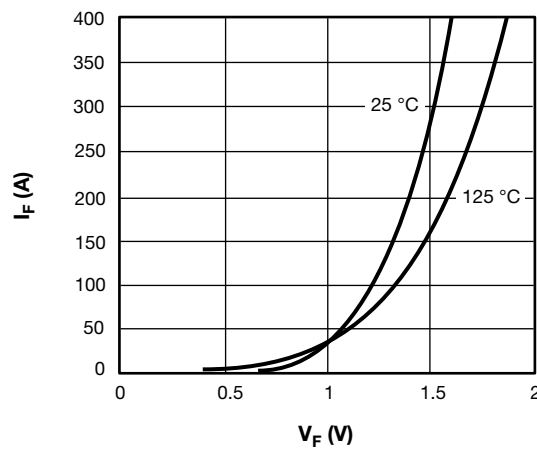


Fig. 9 - Diode Typical Forward Characteristics

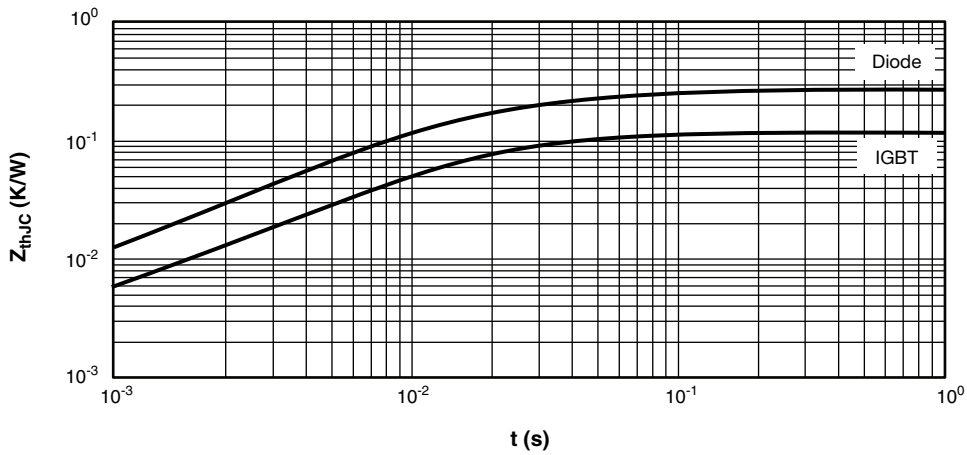
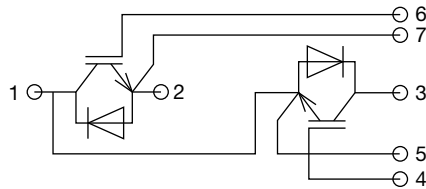


Fig. 10 - Diode Transient Thermal Impedance

CIRCUIT CONFIGURATION

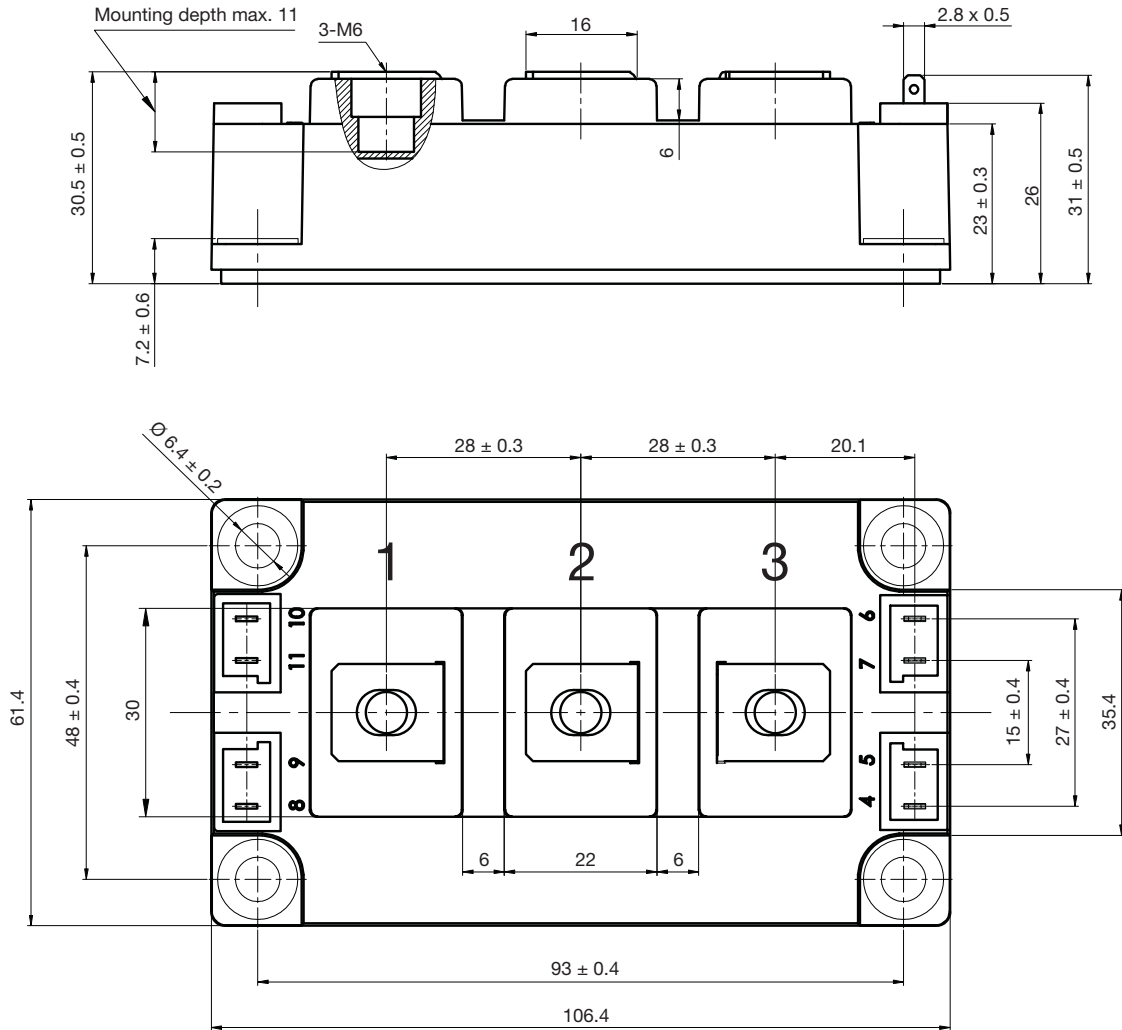


LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95525



Double INT-A-PAK

DIMENSIONS in millimeters (inches)





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