

## Features

- 80MHz RISC MCU and 80MIPS Kalimba DSP
- Internal ROM, serial flash memory and EEPROM interfaces
- Stereo codec with 2 microphone inputs
- Radio includes integrated balun
- 5-band fully configurable EQ
- CSR's latest CVC technology for narrowband and wideband voice connections including wind noise reduction
- Wideband speech supported by HFP v1.6 profile and mSBC codec
- Voice recognition support for answering a call, enables true hands-free use
- Multipoint HFP connection to 2 phones for voice
- Multipoint A2DP connection enables a headset (A2DP) connection to 2 A2DP source devices for music playback
- Secure simple pairing, CSR's proximity pairing and CSR's proximity connection
- Audio interfaces: I<sup>2</sup>S and PCM
- Serial interfaces: UART, USB 2.0 (full-speed), I<sup>2</sup>C and SPI
- SBC, MP3 and AAC decoder support
- Wired audio support (USB and analogue)
- Support for smartphone/tablet applications
- Integrated dual switch-mode regulators, linear regulators and battery charger
- External crystal load capacitors not required for typical crystals
- 3 LED outputs
- 68-ball VFBGA 5.5 x 5.5 x 1mm 0.5mm pitch
- Green (RoHS compliant and no antimony or halogenated flame retardants)

## General Description

BlueCore<sup>®</sup> CSR8640™ BGA is a product from CSR's Connectivity Centre. It is a single-chip radio and baseband IC for Bluetooth 2.4GHz systems including basic rate, EDR to 3Mbps and Bluetooth low energy.

The integrated peripherals reduce the number of external components required, including no requirement for external codec, battery charger, SMPS, LDOs, balun or external program memory, ensuring minimum production costs.

The battery charger architecture enables the CSR8640 BGA to independently operate from the charger supply, ensuring dependable operation for all battery conditions.

## BlueCore<sup>®</sup> CSR8640™ BGA

### CSR8640 Stereo ROM Solution

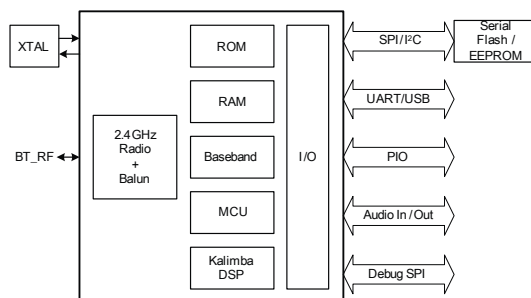
### 2-mic CVC Audio Enhancement

### Fully Qualified Single-chip Bluetooth<sup>®</sup> v4.0 System

#### Production Information

CSR8640A04

Issue 3



## Applications

- Stereo headsets
- Wired stereo headsets and headphones
- Portable stereo speakers

The enhanced Kalimba DSP coprocessor with 80MIPS supports enhanced audio and DSP applications.

The integrated audio codec supports 2 channels of ADC, 2 digital microphone inputs and stereo output, as well as a variety of audio standards.

See *CSR Glossary* at [www.csrsupport.com](http://www.csrsupport.com).

## Device Details

### Bluetooth low energy

- Dual-mode Bluetooth low energy radio
- Support for Bluetooth basic rate / EDR and low energy connections
- 3 Bluetooth low energy connections at the same time as basic rate A2DP

### Bluetooth Radio

- On-chip balun (50Ω impedance)
- No production trimming of external components
- Bluetooth v4.0 specification compliant

### Bluetooth Transmitter

- 9dBm (typical) RF transmit power with level control
- Class 1, Class 2 and Class 3 support, no external PA or TX/RX switch required

### Bluetooth Receiver

- -92dBm (typical)  $\pi/4$  DQPSK receiver sensitivity and -82dBm (typical) 8DPSK receiver sensitivity
- Integrated channel filters
- Digital demodulator for improved sensitivity and co-channel rejection
- Real-time digitised RSSI available to application
- Fast AGC for enhanced dynamic range
- Channel classification for AFH

### Bluetooth Synthesiser

- Fully integrated synthesiser requires no external VCO, varactor diode, resonator or loop filter
- Compatible with crystals 16MHz to 32MHz

### Kalimba DSP

- Enhanced Kalimba DSP coprocessor, 80MIPS, 24-bit fixed point core
- 2 single-cycle MACs; 24 x 24-bit multiply and 56-bit accumulator
- 32-bit instruction word, dual 24-bit data memory
- 6K x 32-bit program RAM including 1K instruction cache for executing out of internal ROM
- 16K x 24-bit + 16K x 24-bit 2-bank data RAM

### Audio Interfaces

- Audio codec with 2 high-quality dedicated ADCs
- Microphone bias generator and up to 2 analogue microphone inputs
- 2 digital microphone inputs (MEMS)
- Enhanced side-tone gain control
- Supported sample rates of 8, 11.025, 16, 22.05, 32, 44.1, 48 and 96kHz (DAC only)

### Auxiliary Features

- Crystal oscillator with built-in digital trimming

### Package Option

- 68-ball VFBGA 5.5 x 5.5 x 1mm 0.5mm pitch

### Physical Interfaces

- UART interface for debug
- USB 2.0 (full-speed) interface for audio and charger enumeration
- 1-bit SPI flash memory interface
- SPI interface for debug and programming
- I<sup>2</sup>C interface for EEPROM
- Up to 22 general purpose PIOs with 3 extra open-drain PIOs available when LED not used
- PCM and I<sup>2</sup>S interfaces
- 3 LED drivers (includes RGB) with PWM flasher independent of MCU

### Integrated Power Control and Regulation

- Automatic power switching to charger when present
- 2 high-efficiency switch-mode regulators with 1.8V and 1.35V outputs direct from battery supply
- 3.3V linear regulator for USB supply
- Low-voltage linear regulator for internal digital circuits
- Low-voltage linear regulator for internal analogue circuits
- Power-on-reset detects low supply voltage
- Power management includes digital shutdown and wake-up commands for ultra-low power modes

### Battery Charger

- Lithium ion / Lithium polymer battery charger
- Instant-on function automatically selects the power supply between battery and USB, which enables operation even if the battery is fully discharged
- Fast charging support up to 200mA with no external components
- Higher charge currents using external pass device
- Supports USB charger detection
- Support for thermistor protection of battery pack
- Support to enable end product design to PSE law:
  - Design to JIS-C 8712/8714 (batteries)
  - Testing based on IEEE 1725

### Baseband and Software

- Internal ROM
- Memory protection unit supporting accelerated VM
- 56KB internal RAM, enables full-speed data transfer, mixed voice/data and full piconet support
- Logic for forward error correction, header error control, access code correlation, CRC, demodulation, encryption bit stream generation, whitening and transmit pulse shaping
- Transcoders for A-law,  $\mu$ -law and linear voice from host and A-law,  $\mu$ -law and CVSD voice over air

## CSR8640 Stereo ROM Solution Details

### Bluetooth Profiles

- Bluetooth v4.0 specification support
- HFP v1.6 wideband speech (HD voice ready)
- HSP v1.2
- A2DP v1.2
- AVRCP v1.4
- Support for smartphone applications (apps)

### Improved Audio Quality

CSR's latest 2-mic CVC audio enhancements for narrowband and wideband connections including:

- 2-mic far-end audio enhancements
- Near-end audio enhancements (noise suppression and AEQ)
- Wind noise reduction
- Packet loss concealment
- Bit error concealment
- Automatic gain control and automatic volume control
- Frequency expansion for improved speech intelligibility
- mSBC codec support for wideband speech

### Music Enhancements

- Configurable 5-band EQ for music playback (rock, pop, classical, jazz, dance etc)
- SBC, MP3, AAC and Faststream decoder
- Stereo widening (S3D)
- Volume Boost

### Additional Functionality

- Support for voice recognition
- Support for multi-language programmable audio prompts
- CSR's proximity pairing and CSR's proximity connection
- Multipoint support for HFP connection to 2 handsets for voice
- Multipoint support for A2DP connection to 2 A2DP sources for music playback
- Talk-time extension

### CSR8600 ROM Series Configuration Tool

Configures the CSR8640 stereo ROM solution software features:

- Bluetooth v4.0 specification features
- Reconnection policies, e.g. reconnect on power-on
- Audio features, including default volumes
- Button events: configuring button presses and durations for certain events, e.g. double press on PIO for last number redial
- LED indications for states, e.g. headset connected, and events, power on etc.
- Indication tones for events and ringtones
- HFP v1.6 supported features
- Battery divider ratios and thresholds, e.g. thresholds for battery low indication, full battery etc.
- Advanced Multipoint settings

### CSR8640 Stereo ROM Solution Development Kit

- CSR8640 stereo ROM solution demonstrator board (DB-8640-10085-1A)
- Interface adapters and cables are available
- Works in conjunction with the CSR8600 ROM Series Configuration Tool and other supporting utilities

## 1 Ordering Information

Device	Package			Order Number
	Type	Size	Shipment Method	
CSR8640 Stereo ROM Solution	VFBGA-68-ball (Pb free)	5.5 x 5.5 x 1mm 0.5mm pitch	Tape and reel	CSR8640A04-IBBC-R

**Note:**

CSR8640 BGA is a ROM-based device where the product code has the form CSR8640Axx. Axx is the specific ROM-variant, A04 is the ROM-variant for CSR8640 Stereo ROM Solution.

Minimum order quantity is 2kpcs taped and reeled.

**Supply chain:** CSR's manufacturing policy is to multisource volume products. For further details, contact your local sales account manager or representative.

### 1.1 Contacts

General information	<a href="http://www.csr.com">www.csr.com</a>
Information on this product	<a href="mailto:Sales@csr.com">Sales@csr.com</a>
Customer support for this product	<a href="http://www.csrsupport.com">www.csrsupport.com</a>
Details of compliance and standards	<a href="mailto:Product.compliance@csr.com">Product.compliance@csr.com</a>
Help with this document	<a href="mailto:Comments@csr.com">Comments@csr.com</a>

### 1.2 CSR8640 Stereo ROM Solution Development Kit Ordering Information

Description	Order Number
CSR8640 Stereo ROM Solution Audio Development Kit	DK-8640-10061-2A

## Document History

Revision	Date	Change Reason
1	03 DEC 10	Original publication of this document.
2	10 DEC 10	Update to microphone support description.
3	06 MAR 12	Production Information.

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